

International Symposium on “Nonvolatile Memory-the Technology Driver of the Electronics Industry”

Agenda

March 26, 2012

Opening Address	
Prof. Yan-Hwa Wu Lee / President, National Chiao Tung University	9:00 ~ 9:10
Deputy Minister, National Science Council	9:10 ~ 9:20
Session I : Floating-gate Memory Technology (A)	
Chairman : Prof. Chung-Yu Wu / Chair Professor, National Chiao Tung University	
Non-Volatile Semiconductor Memory in Retrospect and Prospect	9:20 ~ 10:05
Prof. Simon M. Sze / Chair Professor, National Chiao Tung University	
Coffee Break	10:05 ~ 10:30
Session II : Floating-gate Memory Technology (B)	
Chairman : Dr. Fu-Liang Yang / Director General, National Nano Device Laboratories	
Brief History of ETOX™ NOR Flash Memory	10:30 ~ 11:15
Dr. Stefan K. Lai / Technical Advisor, Xinnova Technology, Inc.	
Future Prospects of NAND Flash Memory Technology – the Evolution from Floating Gate to Charge Trapping to 3D Stacking	11:15 ~ 12:00
Dr. Chih-Yuan Lu / President, Macronix International Co., Ltd.	
Lunch	12:00 ~ 13:00
Session III : FRAM & MRAM	
Chairman : Prof. Ching-Te Chuang / Chair Professor, National Chiao Tung University	
Ferroelectric Random Access Memories	13:00 ~ 13:45
Prof. Hiroshi Ishiwara / Konkuk University	
Spintronics for Non-Volatile Magnetic Random Access Memory	13:45 ~ 14:30
Prof. Stuart A. Wolf / Director, University of Virginia	
Coffee Break	14:30 ~ 14:50
Session IV : PCM & RRAM	
Chairman : Dr. Kuang Yeu Hsieh / Director, Macronix International Co., Ltd.	
Phase Change Memory	14:50 ~ 15:35
Prof. H.-S. Philip Wong / Stanford University	
Redox-Based Resistive Switching Memories	15:35 ~ 16:20
Prof. Rainer Waser / RWTH Aachen University	
Session V : Panel Discussion	16:20 ~ 17:00
Chairman : Prof. Tahui Wang / National Chiao Tung University	